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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/740,012	12/18/2003	Daniel Litaize	81674-307205	2305

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EXAMINER

DOAN, DUC T

ART UNIT PAPER NUMBER

2188

DATE MAILED: 07/25/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

10/740,012

Applicant(s)

LITAIZE ET AL.

Examiner

Duc T. Doan

Art Unit

2188

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 11 May 2006.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 30-33, 35 and 37 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 30-33, 35, 37 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftperson's Patent Drawing Review (PTO-948)                                    | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

## **DETAILED ACTION**

### ***Status of Claims***

Claims 1-37 have been presented for examination in this application. In response to the last Office Action, claims 3035 and 37 have been amended, claims 1-29,34,36 have been cancelled. As the result, claims 30-33,35,37 remain pending in this application.

Claims 30-33,35,37 are rejected.

All rejections and objections not explicitly repeated below are withdrawn.

Applicant's arguments filed 5/11/06 have been fully considered but they are mooted in view of new ground(s) of rejection necessitated by the Applicant's amendments to the claims.

### ***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

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(a) the invention was known or used by other's in this country or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

Claims 30-33,35,37 are rejected under 35 U.S.C. 102 (e) as being anticipated by Murakami (US 4467420).

As for claim 30, Murakami describes a memory component (Murakami's Fig 5; multi ports, multi channels memory component) comprising:  
a random access memory array having a plurality of storage locations (Fig 5: #3 RAM array), a timing input for receiving a timing signal which when active toggles between first and second states at a periodic rate (Fig 5: #5 time divisional control circuit; column 3 line 58 to column 4 line 10 describes control circuit that when activated during DMA operation, generates multiple periodic timing signals, these signals when activated will toggle on and off);  
and circuitry, coupled to the timing input and to the random access memory array, for transferring data to the memory component in synchronism with the timing signal (Fig 5: #P6-0-P6-7 shows circuitry that operate in synchronizing with the clock to transfer data into the multi ports memory component via input ports P60-P6-7),  
and for transferring the data to the random access memory array (Fig 5 show circuitry #G2, #G5 to transfer data from to the random access memory array #3).

As for claim 31, the claim recite wherein the random access memory array is a dynamic random access memory array. Murakami's Fig 5, column 4 lines 30-42, lines 45-65 teaches a method and circuitry to transfer multiple streams of data into a random access memory array. This technique is readily to be applied to any species of random access memory for example, SRAM or DRAM.

As for claim 32, the claim recites wherein said circuitry includes a shift register. Murakami's Fig 7: #22 shift register, #23 serial i/o. It's inherently fact that shift registers are employed to converting serial data into parallel data and vice versa.

As for claim 33, the claim recites wherein said circuitry includes a multiplexer (Murakami's column 3 lines 50-57 clearly discloses the multi channel memory component is capable of time multiplexing RAM data into multiple data streams of ports P60-P67; Murakami Fig 8 shows circuit in which data receiving from RAM #3 and receiving from CPU #1 are multiplexed into a shift register, before shifting out to serial i/o #23).

Claim 35 rejected based on the same rationale as in the rejection of claim 31.

Claim 37 rejected based on the same rationale as in the rejection of claim 33. Murakami's Fig 5 shows circuit G1,G4 that received the data from RAM and for transferring data out of the multi channels memory component, synchronously with the clock, via P60-7. Murakami Fig 8 shows circuit in which data receiving from RAM #3 and receiving from CPU #1 are multiplexed into a shift register, before shifting out to serial i/o #23).

### ***Conclusion***

**THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period

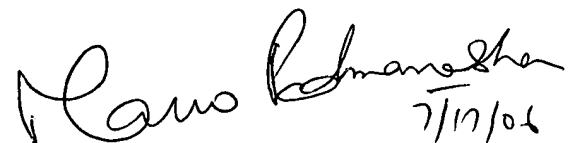
will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

When responding to the office action, Applicant is advised to provide the examiner with the line numbers and page numbers in the application and/or references cited to assist examiner to locate the appropriate paragraphs.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Duc T. Doan whose telephone number is 571-272-4171. The examiner can normally be reached on M-F 8:00 AM 05:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mano Padmanabhan can be reached on 571-272-4210. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



7/17/06

**MANO PADMANABHAN**  
**SUPERVISORY PATENT EXAMINER**